

## REGISTRATION FORM (FDP - FAPSP)

Name

.....

Sex : .....

DOB : .....

Designation : .....

Department : .....

Institution Address

.....

.....

.....

E-mail id : .....

Contact No : .....

### Declaration

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing the FDP (FAPSP). I shall attend the program for the entire duration (11.12.2021 to 24.12.2021)

Date:

Place:

**Signature of the Applicant**

Mr./Ms./Dr..... is an employee of our Institute / Organization and is hereby allowed to attend the FDP.

## ABOUT FDP

Two-Week FDP on “Future Automation of Power System Protection in Indian Power Sector” under the AICTE Quality Improvement Scheme (AQIS) is sanctioned to the Department of Electrical and Electronics Engineering, Anna University, UCE-BIT Campus, Tiruchirappalli, Tamilnadu, India. In view of the prevailing COVID-19 Pandemic Situation, AICTE has directed for organizing two FDPs on the sanctioned topic for facilitating and benefiting the participants from academics and industry across the country. This FDP is designed to enhance the knowledge of participants about current technological developments in the field of protection, monitoring, controlling in power system, future automation technologies and the challenges faced by the today’s power sectors.

### REGISTRATION FEE AND LINK

There is **no registration fee** for attending the FDP programme. However, pre-registration is mandatory for attending the FDP. Academicians/Industry Persons can participate.

**Link:** <https://forms.gle/v55uPajN1VFxjqHt8>

### Coordinators

**Dr.M.Balasingh Moses**

**Dr.H.Habeebullah Sait | Dr.R.Gandhi Raj**

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BIT Campus, Tiruchirappalli, Tamil Nadu.  
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TWO WEEK FACULTY DEVELOPMENT  
PROGRAMME (FDP) ON

## **Future Automation of Power System Protection in Indian Power Sector**

(PHASE II - ONLINE)

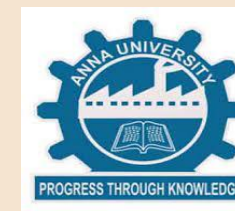
**(11.12.2021 to 24.12.2021)**

Sponsored by



(All India Council for Technical  
Education, New Delhi)

Organized by



**DEPARTMENT OF  
ELECTRICAL AND ELECTRONICS ENGINEERING  
ANNA UNIVERSITY, UCE - BIT CAMPUS  
TIRUCHIRAPPALLI, TAMILNADU, INDIA**

Web: [www.annauniv.edu](http://www.annauniv.edu) / [www.aubit.edu.in](http://www.aubit.edu.in)

## **ANNA UNIVERSITY BIT-CAMPUS, TIRUCHIRAPPALLI**

Anna University, Bharathidasan Institute of Technology (BIT) has been established as a Technical Institute under Anna University, Chennai by the Government of Tamil Nadu. BIT has carved a niche for itself in providing technical education in the disciplines that are rare of their kind in our country with a high level of competence since 1999. It offers a variety of UG, PG and research programs with an approximate of 5000 students in the campus. The main objective of the institute is to generate a horde of highly creative professionals, who can contribute not only to the Human Resource Development and also to Nation building exercise.

## **DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

The Department of EEE was established in 2006 as part of the Anna University, BIT Campus, Tiruchirappalli. The Department is recognized for teaching and for providing an outstanding research environment for staff and students. The Department provides professional training and at the same time to participate actively in applied and theoretical research. The department strives to instill in students the attitudes, values, vision and training that will prepare them for lifetimes of continued learning and leadership, to develop the ability and passion to work wisely, creatively .

and effectively for the benefit of society; to generate new knowledge for the betterment of humankind and disseminate it universally; and to generate realistic and innovative solutions for the current and future technological needs.

## **ABOUT AICTE-AQIS PROGRAMME**

The objectives of this AICTE-AQIS FDP Programme to intends to provide financial assistance to facilitate up-gradation of knowledge, skill and intends to provide opportunities for teachers employed in Technical Institution. The objective is to cover areas such as new concepts, methods and techniques, theory and skills development and up gradation of educational technology, motivation, communication skills, management and other relevant issues to keep pace with the changing scenario in Technical Education.

## **MAJOR AREA TO BE COVERED**

- # Modern Power System Protection
- # Protection in Sustainable Energy
- # Industrial Revolution 4.0 Technologies
- # Protection Challenges in Smart Grid
- # Information and Communication Technology
- # PMU and HVDC Protection
- # Digital Transformation in Power Industry
- # Fault Detection and Diagnosis
- # Future Emerging Automation Technologies

## **ORGANIZING COMMITTEE**

<b>Chief Patron</b>	<b>Dr. R. Velraj</b> Hon'ble Vice Chancellor, Anna University, Chennai
<b>Patron</b>	<b>Dr. G. Ravikumar</b> Registrar, Anna University, Chennai
<b>Chair</b>	<b>Dr. P. Hariharan</b> Director, Centre for Constituent Colleges Anna University, Chennai
<b>Organizing Secretary</b>	<b>Dr. T. Senthil Kumar</b> Dean, UCE, BIT Campus, Anna University Tiruchirappalli
<b>Convener</b>	<b>Dr. P. Anbalagan</b> Head, Department of EEE, UCE, BIT Campus, Anna University, Tiruchirappalli
<b>Co-ordinators</b>	<b>Dr.M. Balasingh Moses</b> <b>Dr.H. Habeebullah Sait</b> <b>Dr.R. Gandhi Raj</b> Department of EEE, UCE, BIT Campus, Anna University, Tiruchirappalli