

One Day Workshop on Analog Discovery Kit

07th JUNE, 2019

Registration Form

Name:

Designation:.....

Department:.....

Institution:.....

Address:

.....

.....

Pin:.....

E-mail:.....

Phone:.....

DOB:.....

Accommodation required: Yes/No.....

Declaration

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing the FDTP. If selected, I shall attend the program for the entire duration.

Signature

Place:
Date:

Sponsorship

Mr./ Ms./ Dr.

is an employee of our Institute / Organization and is hereby allowed to attend the University College of Engineering,BIT Campus,Anna University, Trichy, One Day Workshop on Analog Discovery Kit. He / She will be permitted to attend the program, if selected.

Date:

Place:

Signature & Seal of the HOD/ Principal

Contact

The Co-ordinators,

Department of ECE

Anna University, BIT Campus

Tiruchirappalli - 620 024.

Phone: +91-8668193771, 9025189481,
9840974408, 9940658158

- Online Registration

<https://forms.gle/8PHhHzaso2w7WAPF8>

- Scanned copy of Registration form to be sent: jayasankar27681@gmail.com

- Hard copy should be sent to the coordinator

One Day Workshop

On

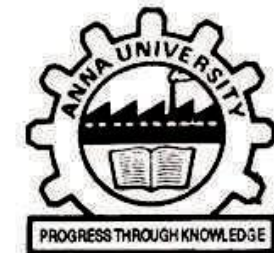
Analog Discovery Kit

07th JUNE 2019

Co-ordinators

Dr.T.Jayasankar

Dr. P.Maheswara Venkatesh



Sponsored by

**PANTECH PROLABS INDIA PVT LTD
CHENNAI-119**

PANTECH SOLUTIONS®
Technology Beyond the Dreams

Organized by

**Department of ECE
University College of Engineering
BIT Campus
Tiruchirappalli - 620 024**

University College of Engineering, BIT Campus, Anna University, Tiruchirappalli

Anna University, BIT Campus, Tiruchirappalli, has been established by the Government of Tamil Nadu as a 'Technical Institution' at Tiruchirappalli - known as Rock Fort city, with rich cultural heritage and home to many reputed educational institutions. The BIT institution is one of the constituent colleges of Anna University, Chennai. The Institution is established with a view to impart quality student-centered technical education, so as to produce proven technocrats and to strive hard for continuous development and improvement in learning, research and developmental activities.

Teaching, Research and Extension services are carried through the Departments and Centers of the Institution. The Institution also works towards fostering partnerships with Academic and Industrial experts to provide quality education and opportunities for entrepreneurial development by providing hands-on training. BIT has carved a niche for itself in providing technical education in the disciplines that are rare of their kind in our country.

About the Department

The Department of Electronics and Communication Engineering at Bharathidasan Institute of Technology started at 2007 offers undergraduate (B.E., Electronics and Communication Engineering) and Postgraduate (M.E., Communication Systems Part Time) programmes providing excellent career opportunities in the wide area of Electronics and Communication Engineering.

The Department has a team of faculty members with excellent academic and research experience. The Department is equipped with state-of-the-art laboratory facilities to aid academic and research experiments.

The major research areas of the Department include Biometrics, Device Modeling, Mobile and Wireless Communications, Multimedia Technology, Network Security, Optical Network, Signal/Image/Speech Processing, VLSI, Wireless Sensor Networks.

Objective

The discovery kit acts as a built-in Oscilloscope, Spectrum analyzer, Protocol analyzer, Network Analyzer, Function Generator, Logic Analyzer, Triggers and Power Source carrying all these equipments on your hand. In this one day workshop you can get hands on learning to use the equipment for debugging, testing and teaching purpose.

Topics to be covered

- Experiments on Basic Electronics, Operational Amplifiers, Communications Circuits, Bode Plots and Spectrum Analyzers
- Simple Op Amps, Op Amp as Comparator, Op Amp Settling Time, Measuring Loop Gain, PN Diode I/V Curves, Differential Temperature Sensor & Zener Diode Regulator.
- Sensor Measurement and Analysis
- Heartbeat, ECG, Loud Speaker impedance profile analysis
- Protocol (UART, IIC, 1 Wire, SPI, Logic and Pattern Analysis)
- Debugging DSP Application, Real Time FIR, IIR and Audio Applications

Resource Person

R& D Engineers from PANTECH PROLABS INDIA PVT LTD, CHENNAI

Registration Fee

There is **no registration fee** for this FDTP (Total Number of participants is strictly restricted to 50 with a minimum of 45 external participants)

Who Can Participate

Faculty from affiliated Engineering Colleges only eligible for this One Day Workshop

Other Terms and Conditions

- No TA/DA will be paid

ORGANIZING COMMITTEE

**CHIEF PATRON : Dr.M.K.Surappa
Vice-Chancellor,
Anna University,
Chennai**

**PATRON : Dr.J.Kumar
Registrar,
Anna University,
Chennai**

**CHAIR : Dr.T.Senthilkumar
Dean
University College of Engineering
BIT campus
Anna University, Trichy**

**CONVENOR : Dr. P Ramadevi
AP/ECE & Head i/c**

**COORDINATORS : Dr.T Jayasankar
Dr.P.Maheswara Venkatesh
AP/ECE**

VENUE : ECE DEPT

Important Dates

Program Date: 07-06-2019

Registration End Date: 05-06-2019

Online Registration Link

<https://forms.gle/8PHhHzaso2w7WAPF8>